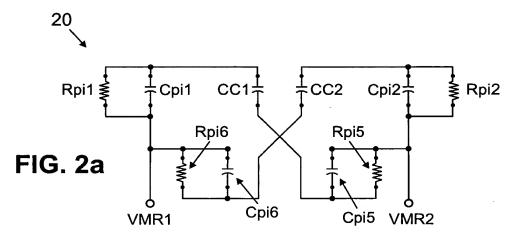
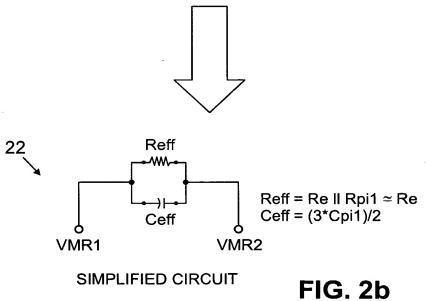


FIG. 1



EQUIVALENT INPUT CIRCUIT

CC1 >> Cpi1 & CC1 >> Cpi2 CC2 >> Cpi1 & CC2 >> Cpi2 CC1 >> Cpi5 & CC1 >> Cpi6 CC2 >> Cpi5 & CC2 >> Cpi6



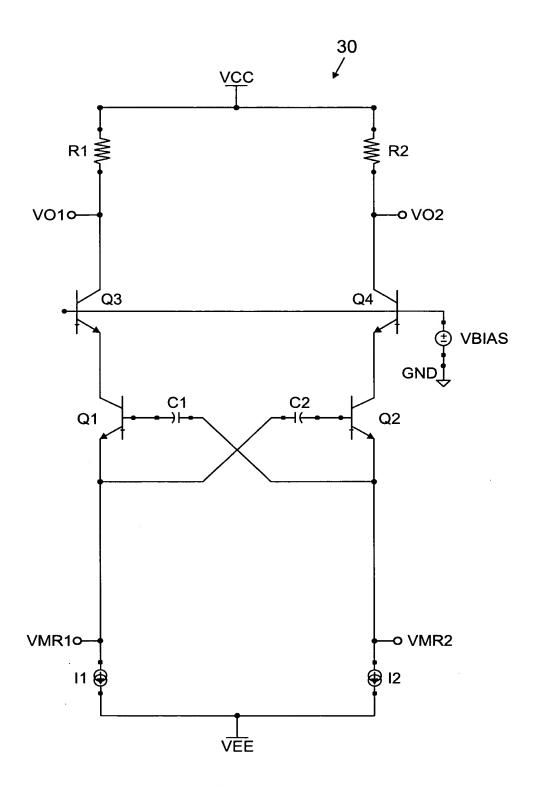
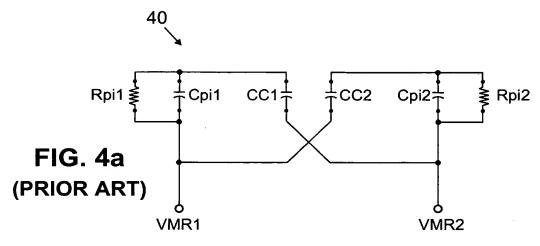


FIG. 3 (PRIOR ART)



EQUIVALENT INPUT CIRCUIT

CC1 >> Cpi1 & CC1 >> Cpi2 CC2 >> Cpi1 & CC2 >> Cpi2

